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BREAKABLE	226
BREAKABLES	3
BREAKABLY	3
BREAKAGE	2222
BREAKAGES	67
BREAKAGING	1
(6167479.PN. AND BREAK\$6).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	0

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 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

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L3 ☐

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Search History

DATE: Thursday, March 04, 2004 [Printable Copy](#) [Create Case](#)

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side by side

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result set

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L3 6167479.pn. and break\$6

0 L3

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Search Results - Record(s) 1 through 20 of 22 returned.

☐ 1. Document ID: US 20030208723 A1

Using default format because multiple data bases are involved.

L11: Entry 1 of 22

File: PGPB

Nov 6, 2003

PGPUB-DOCUMENT-NUMBER: 20030208723

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030208723 A1

TITLE: Automated processor generation system for designing a configurable processor and method for the same

PUBLICATION-DATE: November 6, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Killian, Earl A.	Los Altos Hills	CA	US	
Gonzalez, Ricardo E.	Menlo Park	CA	US	
Dixit, Ashish B.	Mountain View	CA	US	
Lam, Monica	Menlo Park	CA	US	
Lichtenstein, Walter D.	Belmont	MA	US	
Rowen, Christopher	Santa Cruz	CA	US	
Ruttenberg, John C.	Newton	MA	US	
Wilson, Robert P.	Palo Alto	CA	US	
Wang, Albert Ren-Rui	Fremont	CA	US	
Maydan, Dror Eliezer	Palo Alto	CA	US	

US-CL-CURRENT: 716/1

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 2. Document ID: US 20030135789 A1

L11: Entry 2 of 22

File: PGPB

Jul 17, 2003

PGPUB-DOCUMENT-NUMBER: 20030135789

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030135789 A1

TITLE: Method and system for instruction tracing with enhanced interrupt avoidance

h e b b g e e e f e e e f b e

PUBLICATION-DATE: July 17, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
DeWitt, Jimmie Earl JR.	Georgetown	TX	US	
Hussain, Riaz Y.	Austin	TX	US	
Levine, Frank Eliot	Austin	TX	US	
Urquhart, Robert John	Austin	TX	US	

US-CL-CURRENT: 714/38

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWMC	Draw De
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☐ 3. Document ID: US 20020194389 A1

L11: Entry 3 of 22

File: PGPB

Dec 19, 2002

PGPUB-DOCUMENT-NUMBER: 20020194389

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020194389 A1

TITLE: Secure machine platform that interfaces to operating systems and customized control programs

PUBLICATION-DATE: December 19, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Worley, William S. JR.	Centennial	CO	US	
Worley, John S.	Fort Collins	CO	US	
Magenheimer, Daniel J.	Fort Collins	CO	US	
Hyser, Chris D.	Fort Collins	CO	US	
Christian, Tom	Fort Collins	CO	US	
McKee, Bret	Fort Collins	CO	US	
Gardner, Robert	Fort Collins	CO	US	

US-CL-CURRENT: 719/310

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWMC	Draw De
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☐ 4. Document ID: US 20010052053 A1

L11: Entry 4 of 22

File: PGPB

Dec 13, 2001

PGPUB-DOCUMENT-NUMBER: 20010052053

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20010052053 A1

TITLE: Stream processing unit for a multi-streaming processor

PUBLICATION-DATE: December 13, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Nemirovsky, Mario	Saratoga	CA	US	
Melvin, Stephen	San Francisco	CA	US	

US-CL-CURRENT: 711/138; 711/125, 711/126

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Draw De
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☐ 5. Document ID: US 20010023479 A1

L11: Entry 5 of 22

File: PGPB

Sep 20, 2001

PGPUB-DOCUMENT-NUMBER: 20010023479

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20010023479 A1

TITLE: Information processing unit, and exception processing method for specific application-purpose operation instruction

PUBLICATION-DATE: September 20, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Kimura, Michihide	Kawasaki		JP	
Suga, Atsuhiko	Kawasaki		JP	
Miyake, Hideo	Kawasaki		JP	
Imai, Satoshi	Kawasaki		JP	
Nakamura, Yasuki	Kawasaki		JP	

US-CL-CURRENT: 712/209; 712/218, 712/228, 712/244

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Draw De
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☐ 6. Document ID: US 6587967 B1

L11: Entry 6 of 22

File: USPT

Jul 1, 2003

US-PAT-NO: 6587967

DOCUMENT-IDENTIFIER: US 6587967 B1

TITLE: Debugger thread monitor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Draw De
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☐ 7. Document ID: US 6567933 B1

L11: Entry 7 of 22

File: USPT

May 20, 2003

US-PAT-NO: 6567933

DOCUMENT-IDENTIFIER: US 6567933 B1

TITLE: Emulation suspension mode with stop mode extension

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 8. Document ID: US 6557116 B1

L11: Entry 8 of 22

File: USPT

Apr 29, 2003

US-PAT-NO: 6557116

DOCUMENT-IDENTIFIER: US 6557116 B1

TITLE: Emulation suspension mode with frame controlled resource access

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 9. Document ID: US 6553513 B1

L11: Entry 9 of 22

File: USPT

Apr 22, 2003

US-PAT-NO: 6553513

DOCUMENT-IDENTIFIER: US 6553513 B1

TITLE: Emulation suspend mode with differing response to differing classes of interrupts

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 10. Document ID: US 6477683 B1

L11: Entry 10 of 22

File: USPT

Nov 5, 2002

US-PAT-NO: 6477683

DOCUMENT-IDENTIFIER: US 6477683 B1

TITLE: Automated processor generation system for designing a configurable processor and method for the same

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 11. Document ID: US 6378125 B1

L11: Entry 11 of 22

File: USPT

Apr 23, 2002

US-PAT-NO: 6378125

DOCUMENT-IDENTIFIER: US 6378125 B1

**** See image for Certificate of Correction ****TITLE: Debugger thread identification points

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 12. Document ID: US 6378124 B1

L11: Entry 12 of 22

File: USPT

Apr 23, 2002

US-PAT-NO: 6378124

DOCUMENT-IDENTIFIER: US 6378124 B1

TITLE: Debugger thread synchronization control points

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 13. Document ID: US 6341324 B1

L11: Entry 13 of 22

File: USPT

Jan 22, 2002

US-PAT-NO: 6341324

DOCUMENT-IDENTIFIER: US 6341324 B1

TITLE: Exception processing in superscalar microprocessor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 14. Document ID: US 6178492 B1

L11: Entry 14 of 22

File: USPT

Jan 23, 2001

US-PAT-NO: 6178492

DOCUMENT-IDENTIFIER: US 6178492 B1

**** See image for Certificate of Correction ****

TITLE: Data processor capable of executing two instructions having operand interference at high speed in parallel

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 15. Document ID: US 6112289 A

L11: Entry 15 of 22

File: USPT

Aug 29, 2000

US-PAT-NO: 6112289

DOCUMENT-IDENTIFIER: US 6112289 A

**** See image for Certificate of Correction ****

TITLE: Data processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. De
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☐ 16. Document ID: US 6094729 A

L11: Entry 16 of 22

File: USPT

Jul 25, 2000

US-PAT-NO: 6094729

DOCUMENT-IDENTIFIER: US 6094729 A

TITLE: Debug interface including a compact trace record storage

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. De
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☐ 17. Document ID: US 5978902 A

L11: Entry 17 of 22

File: USPT

Nov 2, 1999

US-PAT-NO: 5978902

DOCUMENT-IDENTIFIER: US 5978902 A

**** See image for Certificate of Correction ****TITLE: Debug interface including operating system access of a serial/parallel debug port

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. De
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☐ 18. Document ID: US 5717946 A

L11: Entry 18 of 22

File: USPT

Feb 10, 1998

US-PAT-NO: 5717946

DOCUMENT-IDENTIFIER: US 5717946 A

TITLE: Data processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. De
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☐ 19. Document ID: US 5680568 A

L11: Entry 19 of 22

File: USPT

Oct 21, 1997

US-PAT-NO: 5680568

DOCUMENT-IDENTIFIER: US 5680568 A

TITLE: Instruction format with sequentially performable operand address extension modification

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 20. Document ID: US 5201039 A

L11: Entry 20 of 22

File: USPT

Apr 6, 1993

US-PAT-NO: 5201039

DOCUMENT-IDENTIFIER: US 5201039 A

TITLE: Multiple address-space data processor with addressable register and context switching

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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Term	Documents
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SPECIFICS	19327
ASIC	32779
ASICS	10614
APPLICATION\$1	0
APPLICATION	4424707
APPLICATIONA	94
APPLICATIONB	11
APPLICATIONC	21
APPLICATIOND	17
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